

2.5A, 3.0MHz Switching Charger with Dynamic Power Path in 8-pin ESOP

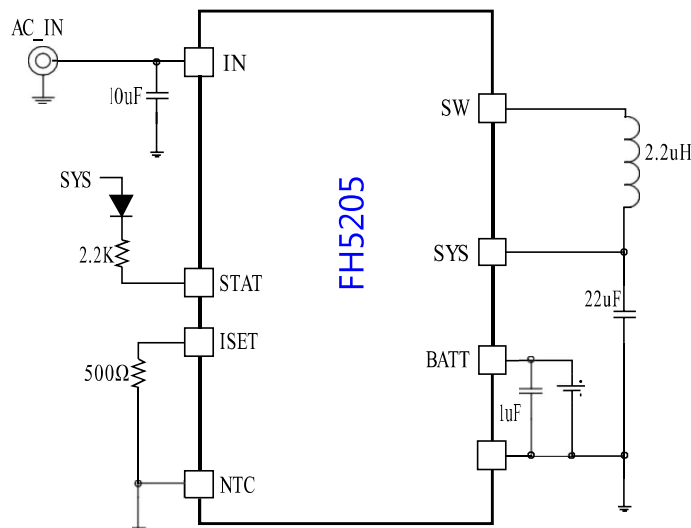
DESCRIPTION

FH5205 is a switching Li-Ion battery charger with dynamic power-path control and input current limiting.

When a battery is connected, depending on the battery voltage, the DC-DC switching regulator either pre-conditions, fast-charges the battery or just regulates a system voltage (V_{SYS}) to a preset voltage. It does not require an external sense resistor for current sensing.

The fast-charging current is determined by programming ISET pin. When the battery voltage reaches the termination voltage i.e. 4.20V, the charging path disconnects SYS to BATT. The FH5205 also includes a dynamic power path when the SYS load current exceeds current limit of the DC-DC regulator internally set, the SYS voltage falls below VBATT, FH5205 turns on the power-path to supplement the system load through the battery.

TYPICAL APPLICATION



2.0A Switching Charger with Minimum Component Count

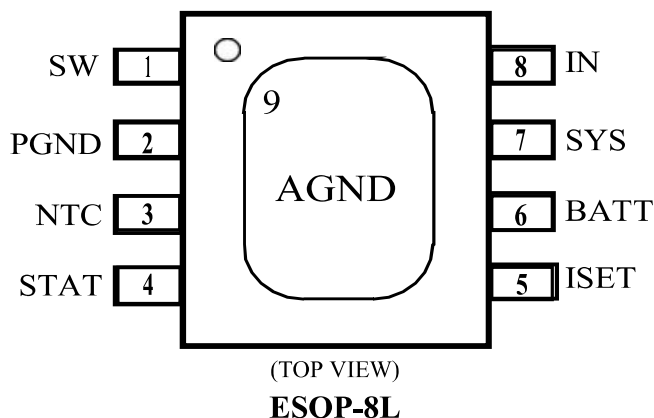
FEATURES

- Switching Charger with Power Path Management
- Up to 95% DC-DC Efficiency
- 50mΩ Power Path MOSFET
- Up to 2.5A Max charging current
- Instant on with a dead Battery or no Battery
- No battery detection
- No External Sense resistor
- Programmable Charging Current

APPLICATIONS

- Tablet, MID
- Smart Phone
- Power Bank

■ PIN CONFIGURATION

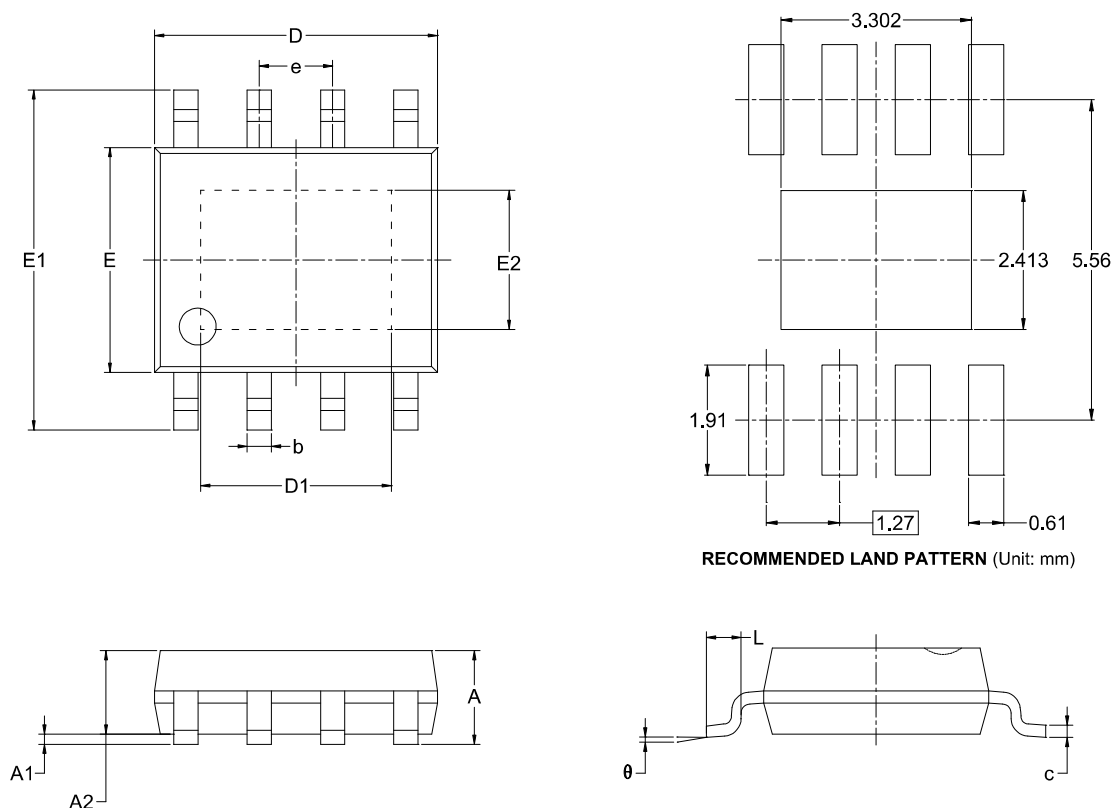


■ PIN DESCRIPTION

PIN #	NAME	DESCRIPTION
1	SW	Switching node of the Switching Regulator. Connect a 1.0uH to 2.2uH inductor from this pin to SYS.
2	PGND	Power Ground Pin. Bypass with a 10uF capacitor to IN.
3	NTC	Thermistor input
4	STATS	Status pin for Charging status indications. An open drain device capable of driving 10mA current.
5	ISET	Fast Charge Current set pin. Connecting a Resistor between ISET to GND This sets the fast charge current value.
6	BATT	Battery pin. Connect a Battery to this pin.
7	SYS	System Voltage Pin. It is also the Switching regulator’s output pin. Connect an inductor and capacitor to form the output filter.
8	IN	Input pin. Can be connected to an AC adaptor or a USB charger output. Bypass with a 10uF capacitor each to PGND
9 (EP)	AGND	Exposed pad for analog ground connection. Must be connected to PGND on PCB.

■ PACKAGE OUTLINE

- Type: ESOP-8L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A		1.700		0.067
A1	0.000	0.100	0.000	0.004
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
D1	3.202	3.402	0.126	0.134
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
E2	2.313	2.513	0.091	0.099
e	1.27 BSC		0.050 BSC	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

■ ORDERING INFORMATION

PART NUMBER	Operating Temperature Range	PACKAGE TYPE	TOP MARK	SPQ
FH5205S08	-40°C to 85°C	ESOP-8L	FH5205 YW *** (YW: date code)	2500PCS/Reel

- FH5205 devices are Pb-free and RoHs compliant.
- The surface prints of our semiconductor devices are subject to change during the production process and do not involve changes in electrical parameters, and we will not separately state the notice.

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➤ Update by Oct.2018